Task 6.1: Modeling of Ge / InGaAs FinFETs

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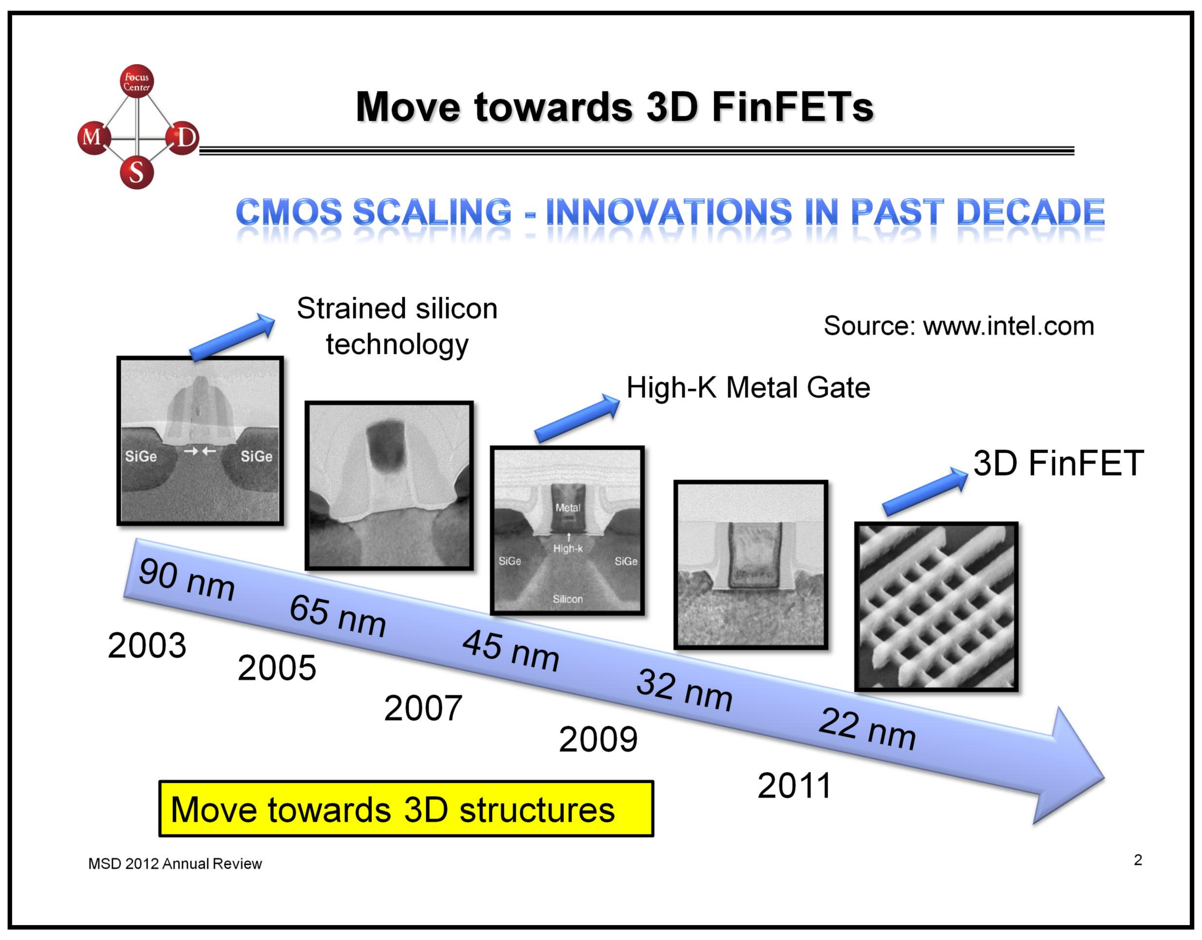

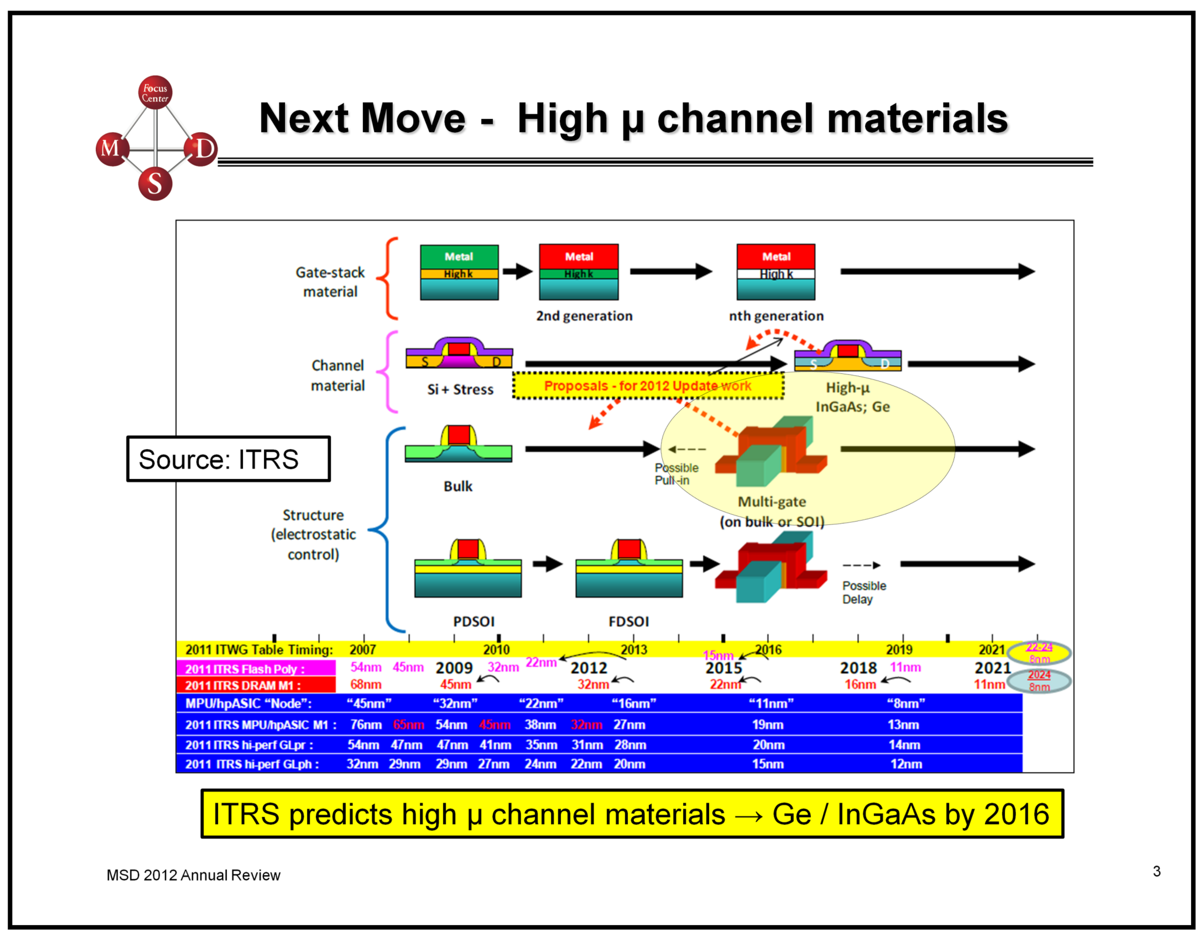

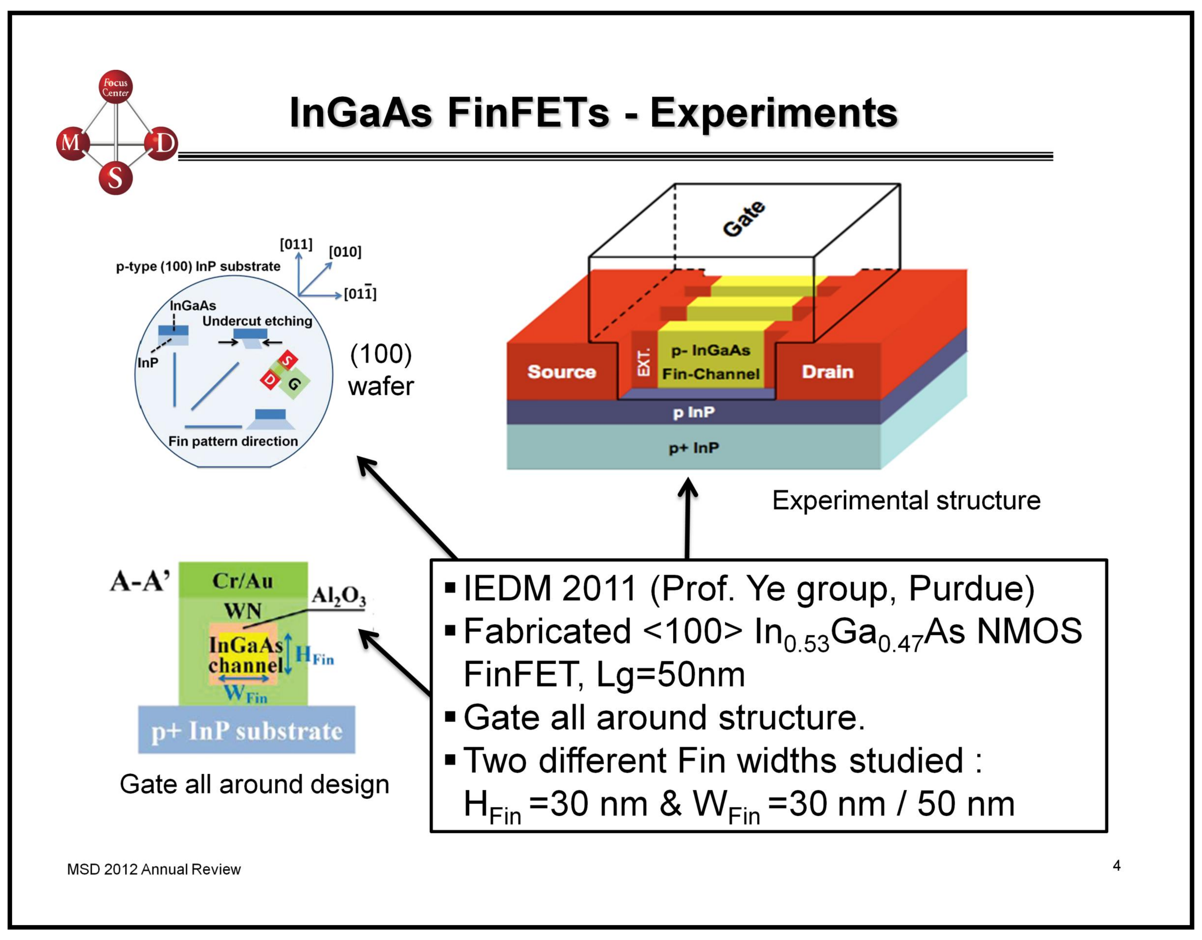

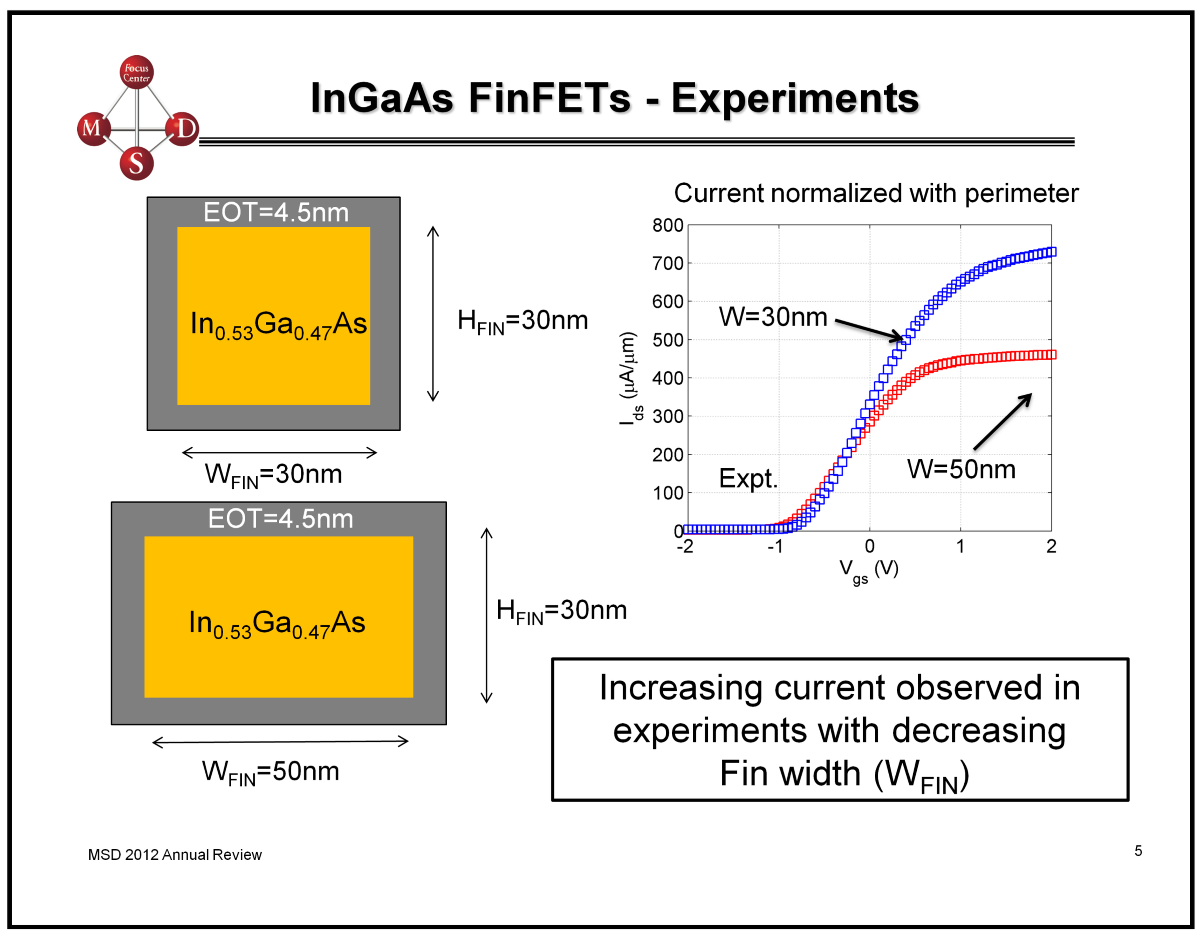

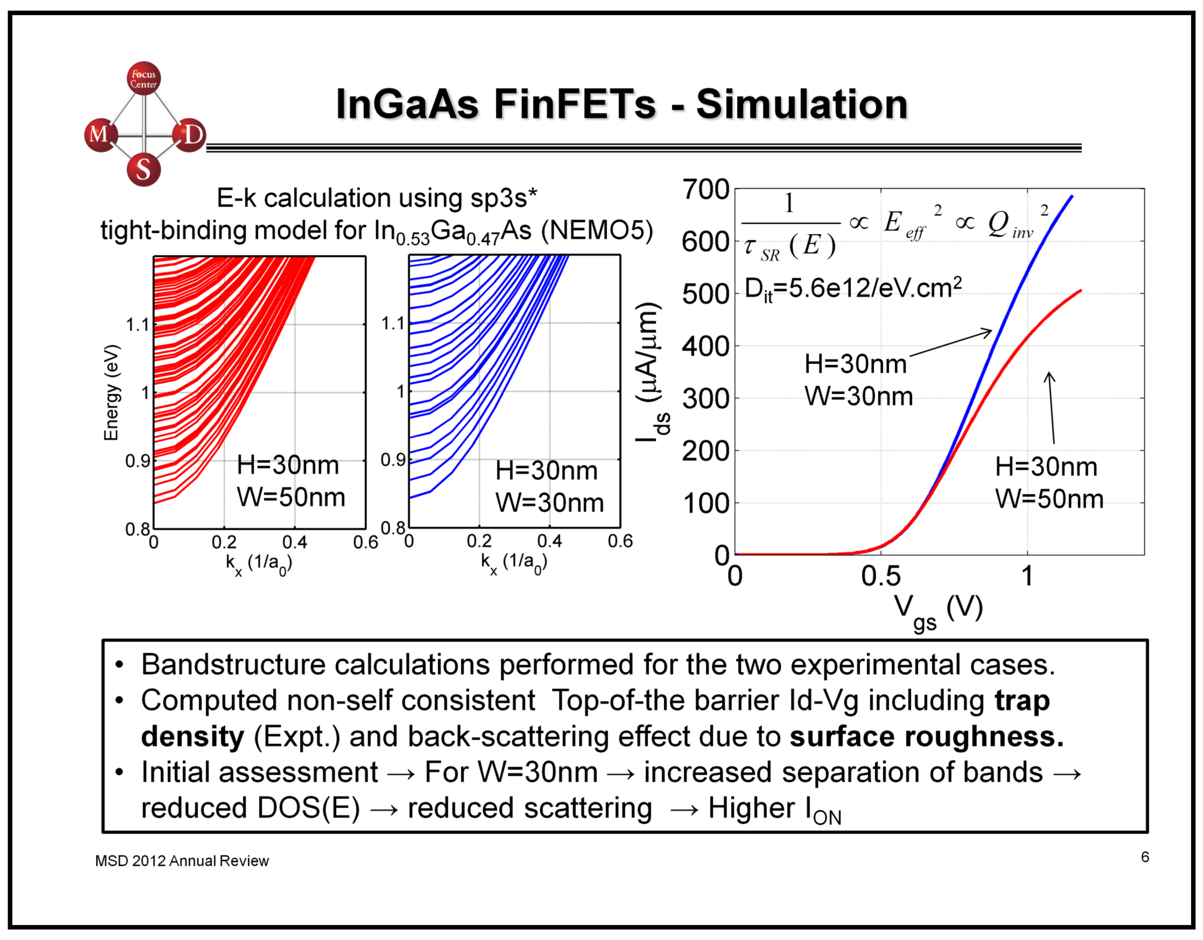

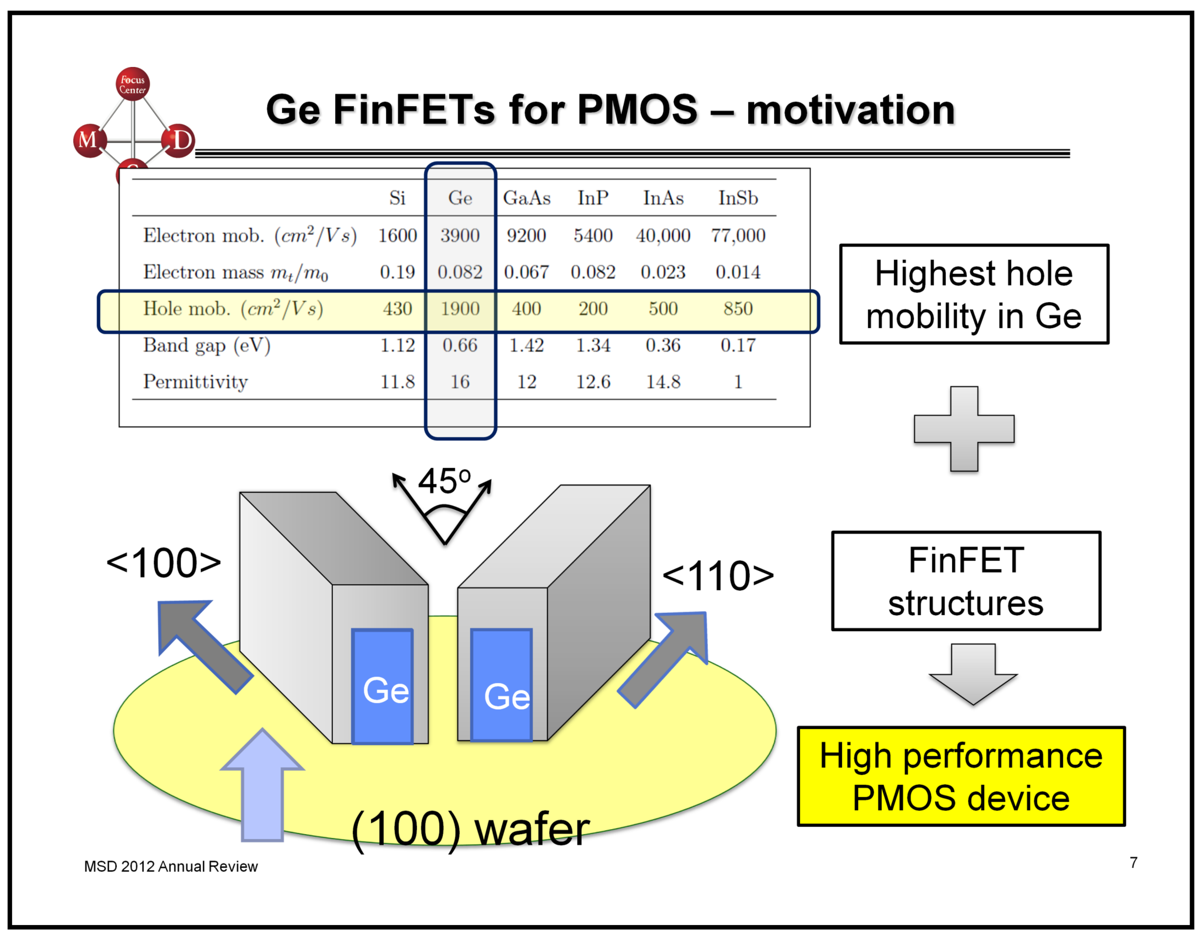

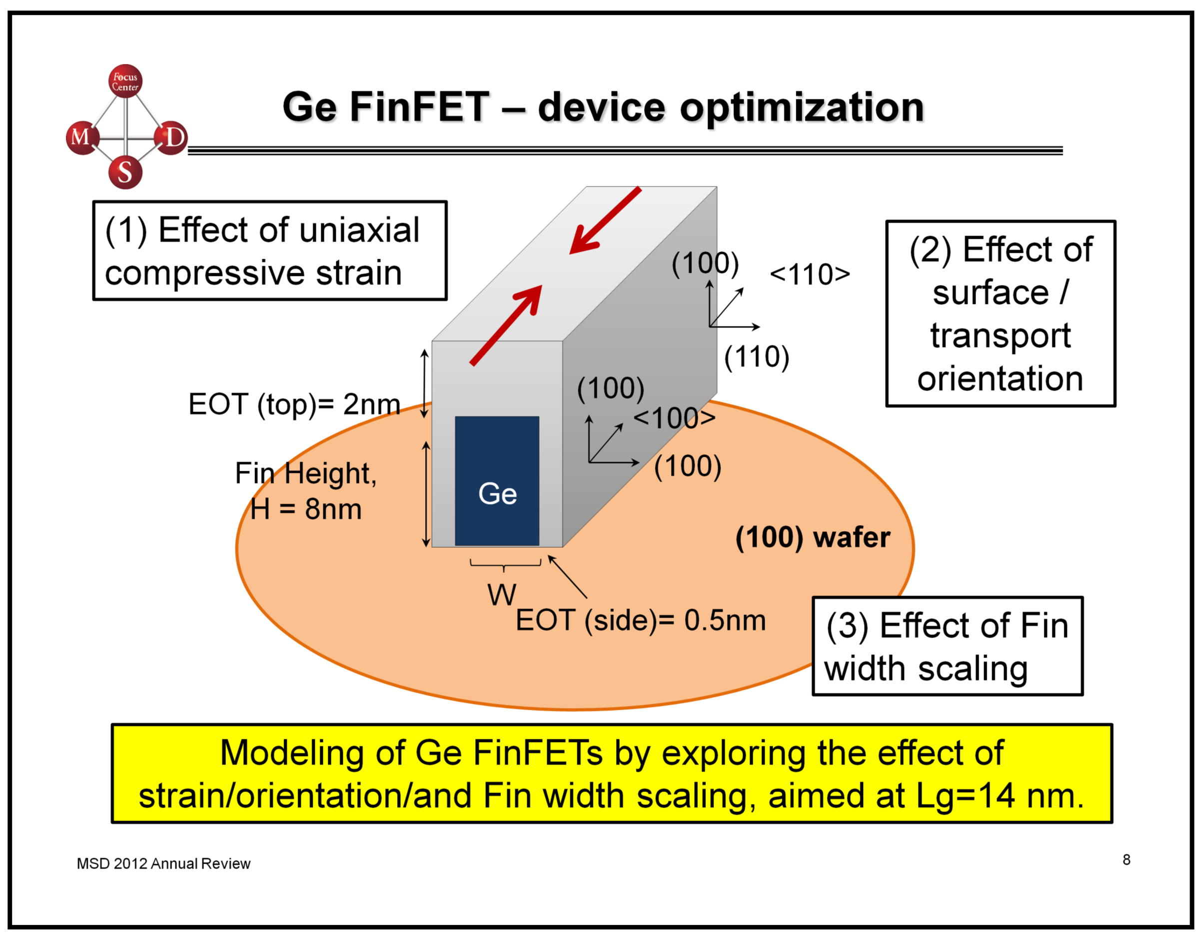

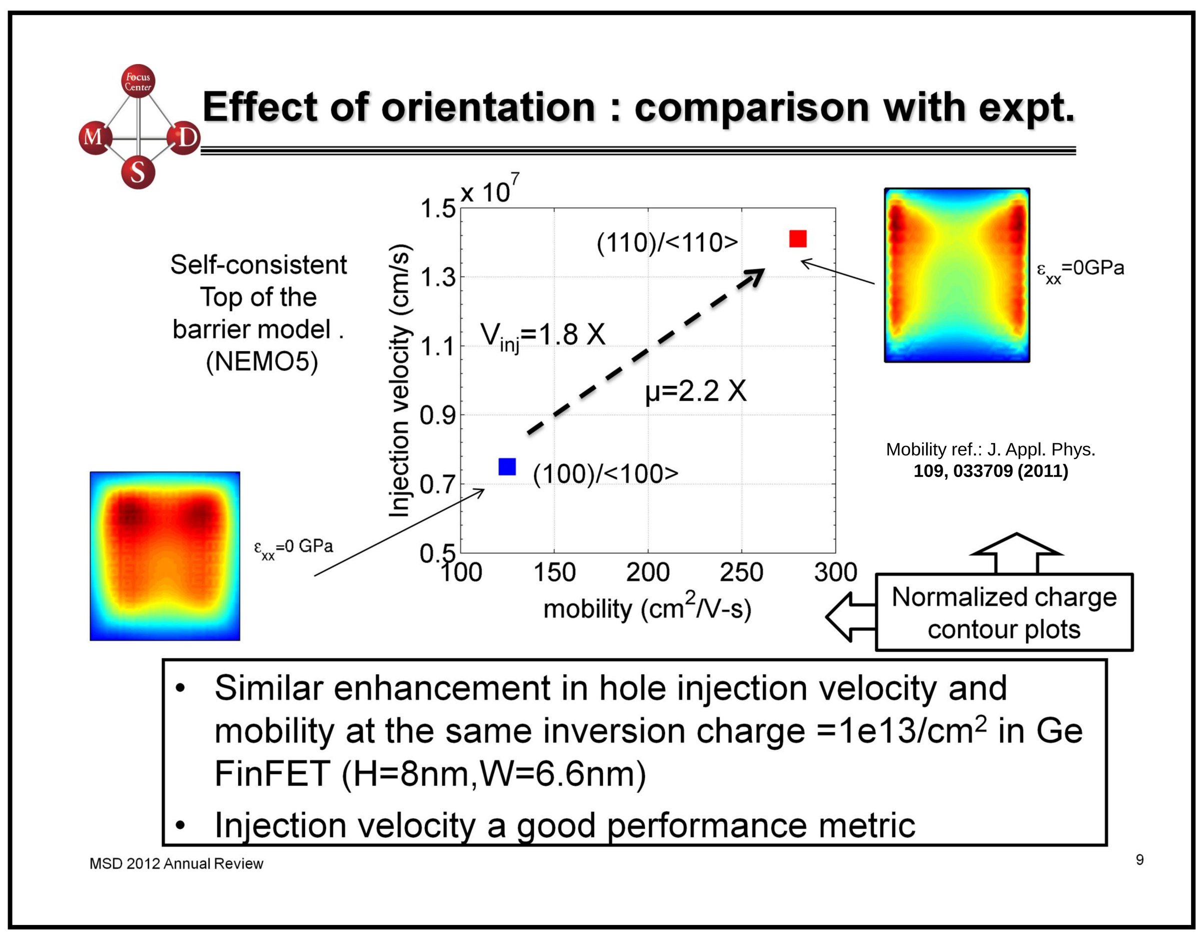

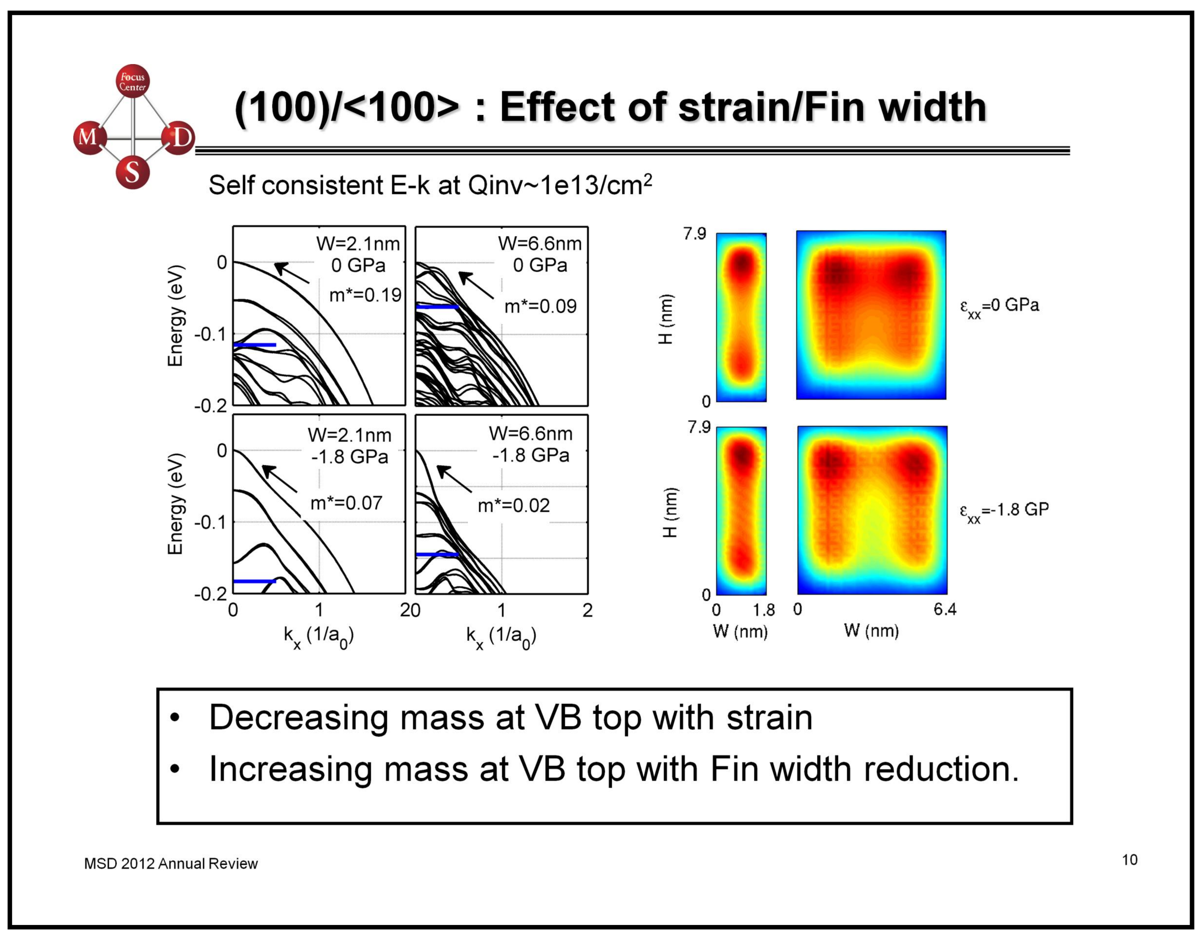

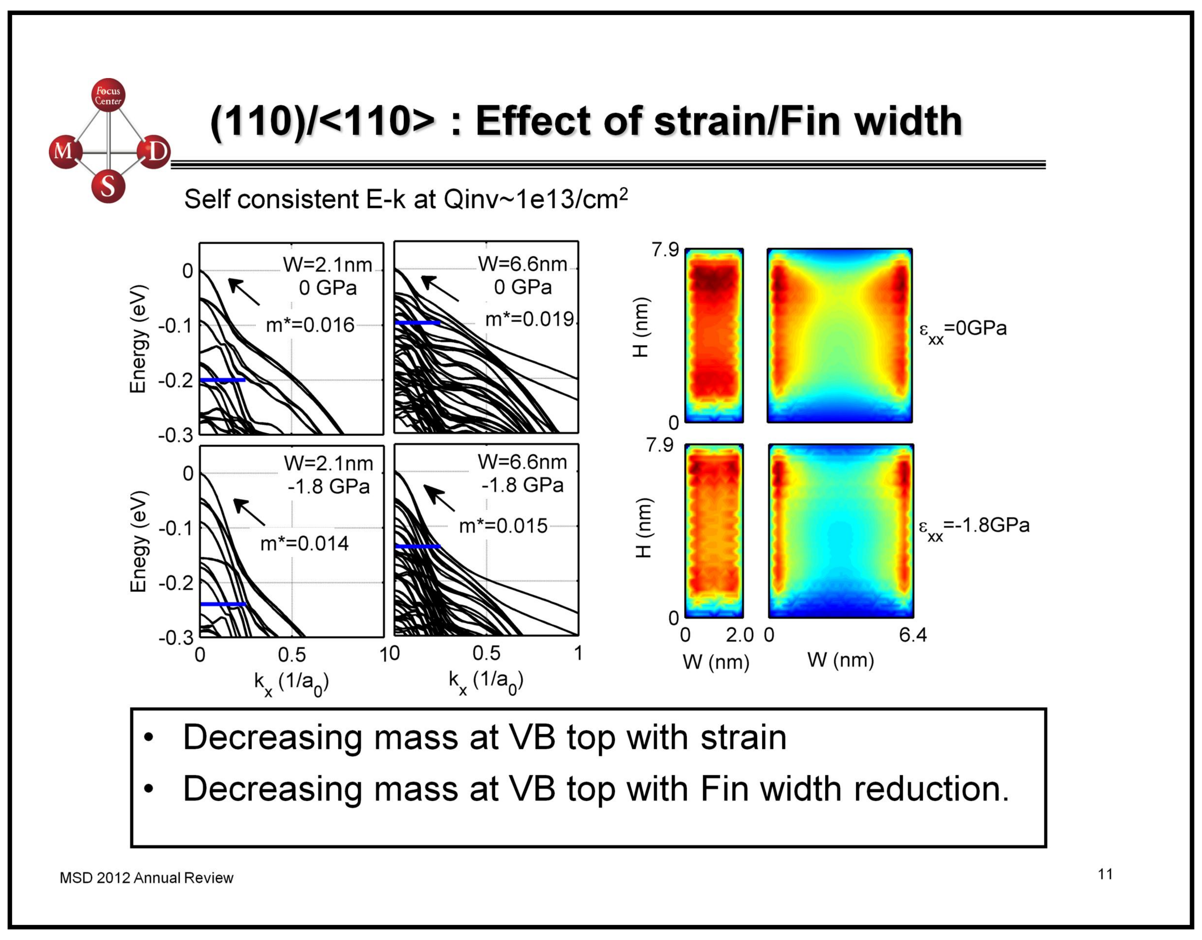

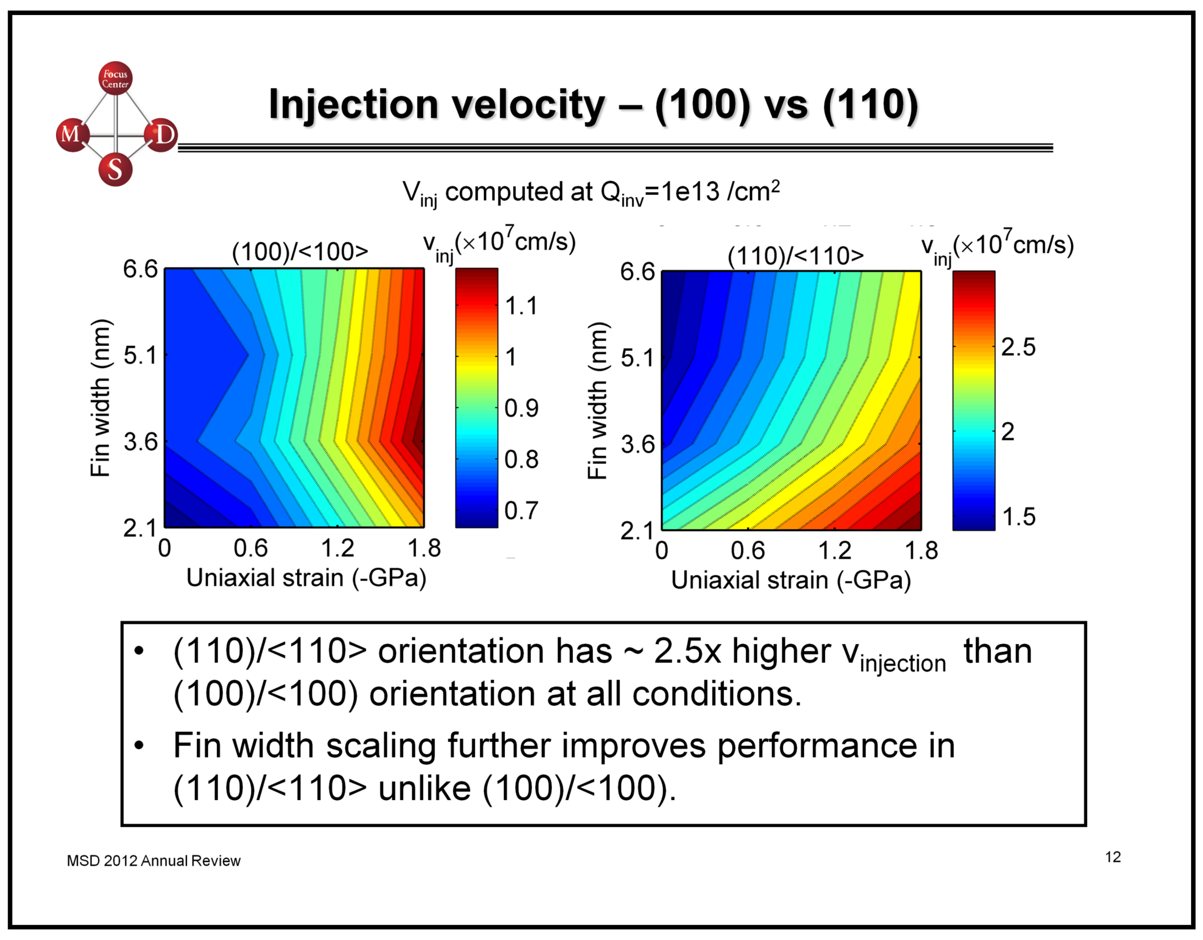

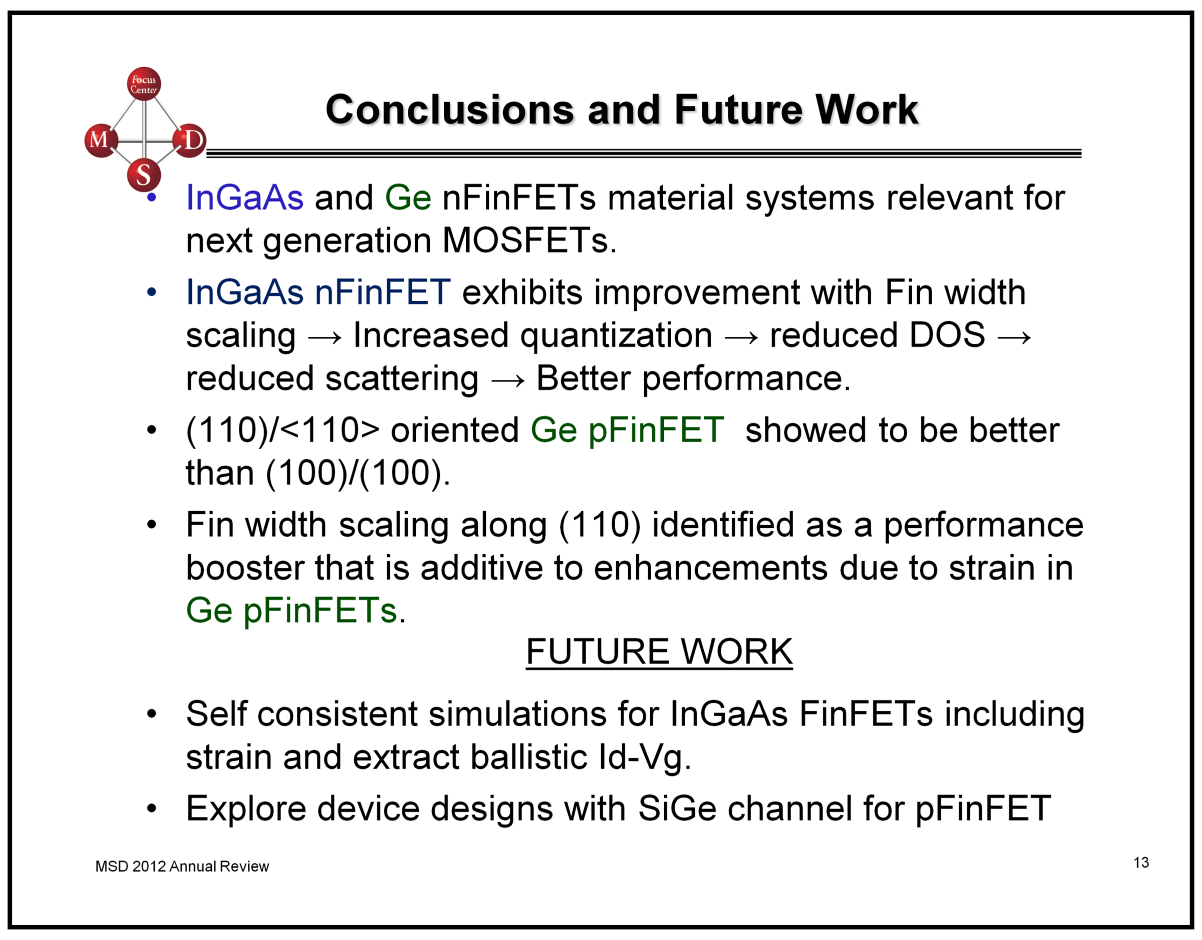

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